

## Addendum

### 1. PACKET SWITCHING DEVICE AND CELL TRANSFER CONTROL METHOD

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PACKET SWITCHING DEVICE AND CELL  
TRANSFER CONTROL METHOD

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a packet switching device and a fixed length packet transfer control method, and more particularly to an ATM (Asynchronous Transfer Mode) packet switching device having a congestion control function and a cell transfer control method in an ATM network.

Description of the Related Art

10           The transfer of fixed length packets (hereinafter referred to as a "cell") is described, for example, in "Data Communication Using ATM: Architectures, Protocols, and Resource Management", IEEE Communication Magazine, August 1994, p24-33 and  
15 "SVC Signaling: Calling All Nodes", DATA COMMUNICATIONS, June 1995, p123-128, and so on.

In an ATM network, a call (connection) is set up by signaling processing performed upon initiating the call along a communication path from a transmitting  
20 device (source terminal) to a receiving device (destination terminal) through a switching device (switch) serving as a transfer path of user cells, and the transfer of the user cells is controlled based on

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A call setup procedure is described, for example, in ITU-T Standard Q.2931, and the call setup procedure is executed to set connection information to a transmitting device, each node (switch) on a path, and a receiving device. The connection information includes identifiers for identifying a call on each of links between the transmitting device and a switch, between switches, and between a switch and the receiving device, a traffic class indicative of a priority of cell transfer in a switch, and so on. The identifiers for identifying a connection (call) are referred to as "VPI" (Virtual Pass Identifier) and "VCI" (Virtual Connection Identifier) and set in a header portion of each cell as address information.

Each switch searches for connection information necessary for the switching operation based on VPI and VCI in each of input cells received through a transmission path. The connection information includes, for example, internal routing information (output port number), identifiers to be attached to an output cell (output VPI/VCI), a traffic class indicative of a cell priority in the switch, and so on.

25           The traffic class indicative of a cell  
priority is described, for example, in "Multimedia  
Traffic Management Principles for Guaranteed ATM

Network Performance", IEEE JOURNAL ON SELECTED AREAS IN COMMUNICATIONS, Vol. 8, No. 3, April 1990, p437-446, and "Traffic Management for B-ISDN Services" IEEE Network, September 1992, p10-19.

5           The traffic class indicative of a cell  
priority includes two: CBR (Constant Bit Rate) and VBR  
(Variable Bit Rate). CBR is a traffic class in which a  
predetermined cell transfer rate is contracted between  
a network and a terminal upon setting up a call so that  
10 the network side guarantees the cell transfer at the  
contracted transfer rate. VBR is a traffic class which  
tolerates a certain degree of statistical variations  
for a transfer rate contracted with a terminal. The  
scheme which controls traffic with a contract  
15 previously made between a network and a terminal is  
referred to as "preventive control".

As traffic for transmitting cells utilizing a  
remaining portion of a bandwidth allocated to another  
terminal in the above-mentioned CBR and VBR without  
20 making a special contract related to the transfer rate  
between a network and a terminal upon setting up a  
call, there is a group of traffic classes referred to  
as "best effort control". The transfer rate contract  
is not made primarily because it is difficult for a  
25 terminal outputting burst traffic to predict the  
characteristic of the traffic upon setting up a call.

The group of best effort control traffics  
includes a UBR (Unspecified Bit Rate) traffic class in

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which a network does not guarantee anything with respect to cell transfer, and an ABR (Available Bit Rate) traffic class which performs a feedback control between a network and a terminal during congestion to  
5 guarantee the prevention of cell loss. The ABR traffic class is described, for example, in "Rate-Based Flow Control Framework for the Available Bit Rate ATM Service", IEEE Network, March/April 1995, p25-39.

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A configuration of a switch for controlling a  
10 transfer in accordance with a traffic class is described, for example, in JP-A-6-197128. In a packet switching scheme described in this publication, each output port is provided with two output buffers, one for CBR traffic class and the other for VBR traffic  
15 class, and information indicative of an empty/full state of the two buffers are stored as table information corresponding to each output port, such that an input buffer control unit references the table information to determine a buffer for accumulating  
20 cells destined to each output port.

In this case, the output priority of cells accumulated in CBR buffer is ranked higher than that of the VBR buffer so that a communication delay time in a switch can be limited within a predetermined value for  
25 a group of cells of the CVR traffic which has strict restrictions to communication delay. In addition, if the CBR buffer does not have any free space, for

example, cells may be accumulated in the VBR buffer, provided that it has some free space, to make good use of a bandwidth in the switch. Further, for supporting the ABR and UBR traffic classes, an output buffer supporting further traffic classes may be additionally provided in addition to the CBR and VBR traffic classes.

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~~THE~~ SUMMARY OF INVENTION

As described above, while several traffic classes have already been proposed in asynchronous communication, it is desired to perform a transfer control by further fractionizing the characteristics in each traffic class, in addition to controlling how a plurality of these traffic classes are properly used.

However, taking as an example the UBR traffic classes which do not give special guarantee to a cell transfer, when a network falls into congestion, a conventional system does not have any means for controlling the Quality of Service belonging to these traffic classes.

It is an object of the present invention to provide a packet processing apparatus and an ATM cell transfer control method which can control the Quality of Service when congestion occurs for a group of best effort control traffic classes which do not make a contract with respect to a transfer rate between a network and a terminal upon setting up a call.

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It is another object of the present invention to provide a node such as an ATM switching device and an ATM cell transfer control method which can control selective cell discard when congestion occurs for a group of traffic classes which have difficulties in making bandwidth reservation from a terminal device upon setting up a call.

To achieve the above objects, a cell transfer control method of the present invention storing information indicative of a priority related to cell discard declared from a source unit in any of nodes in the network corresponding to an identifier of the connection, when setting up a connection belonging to a particular traffic class which does not make bandwidth reservation, such that, when congestion occurs on the connection, the node performs selective discard processing on cells belonging to the particular traffic class in conformity to a predetermined discard condition determined by a relationship between the status of the congestion and the priority.

The node stepwisely changes a cell discard priority class, for example, in accordance with the congestion status, and determines whether or not each cell belonging to the particular traffic class is discarded in conformity to a predetermined discard condition determined by the priority and the cell discard priority class.

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In this case, the node may judge whether or not a data block included in a data portion of each cell of the particular traffic class is divided from the same transmission message as a data portion of a previous cell, and perform the discard processing on cells falling under the discard condition in units of transmission message.

As for the cell discard in units of message, the discard processing is started on cells which fall under a predetermined discard condition determined, for example, by a relationship between the congestion status and the priority, and the discard processing is continued on subsequent cells including part of the same transmission message as data portions of already discarded cells, even if the subsequent cells deviate from the discard condition due to a change in the congestion status. As a modification to the cell discard in units of message, for example, cells including data blocks of the same transmission message as data portions of previously sent cells may be excluded from cells to be discarded, within cells falling under the discard condition, and the discard processing may be started from a cell including a head data block of a subsequent new message.

Also, the present invention provides a packet switching device connected to a plurality of input lines and to a plurality of output lines for

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transferring each fixed length packet (cell) inputted  
from each input line to any output line determined by  
cell header information, which is characterized by  
comprising means, operative when setting up a  
5 connection belonging to a particular traffic class  
which does not make bandwidth reservation, for storing  
information indicative of a priority related to cell  
discard declared from a calling unit as sub-class  
information corresponding to an identifier of the  
10 connection, means for detecting a congestion status on  
each of the output lines, and means for selectively  
performing discard processing on a cell belonging to  
the particular traffic class in conformity to a  
predetermined discard condition determined by a  
15 relationship between a congestion status on an output  
line, to which the cell is to be transferred, and the  
priority.

More specifically, the packet switching  
device of the present invention comprises switch means  
20 having a plurality of input ports and a plurality of  
output ports for transferring a fixed length packet  
(cell) inputted from each input port to any output port  
determined by cell header information, input line  
interfaces each connected between one of the input  
25 ports and an input line, output line interfaces each  
connected between one of the output ports and an output  
line, a call control unit connected to the switch means  
and each of the input line interfaces for transmitting

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and receiving a call control cell to and from the switch means and for transmitting control information including header rewrite information to each of the input interfaces, and congestion monitor means for  
5 detecting a congestion status of output cells on each of the output ports and notifying each of the input interfaces of a detected congestion status as congestion status information, and is characterized in that the call control unit includes means, operative  
10 when setting a connection belonging to a particular traffic class which does not make bandwidth reservation, for notifying an input interface accommodating a calling unit, which is a requestor of the connection setup, of identification information on  
15 the connection and control information including traffic class information declared by a control message from the calling unit and sub-class information indicative of a priority related to cell discard, and each of the input interfaces includes cell discard  
20 control means for selectively discarding user cells belonging to the particular traffic class received from each input line after the connection is set up, in conformity to a predetermined discard condition determined by a congestion status at a destination  
25 output port of the user cell revealed from the congestion status information and the priority related to cell discard notified from the call control unit.

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Each of the input line interfaces includes header conversion means for rewriting header information of an input cell from each input line, and input buffer means for temporarily accumulating head  
5 converted cells, and the cell discard control means selectively accumulates input cells belonging to the particular traffic class in the input buffer means in conformity to the discard condition.

Also, the switch means includes output buffer  
10 means corresponding to the each output port, and means for distributing each of user cells having a head converted by the each input line interface to either of the output buffer means specified by header information, and the congestion monitor means detects a  
15 congestion status of the output cell from an accumulation situation of user cells in the each output buffer means. In addition, the switch means may include a plurality of output buffer means for each output port so as to allocate one of the output buffer  
20 means to cells of a CBR traffic class which guarantees a transfer rate.

According to the configuration of the present invention, the priority information related to cell discard has been previously defined as a sub-class for  
25 a traffic class which does not make bandwidth reservation such as the aforementioned best effort control traffic class group. Upon setting up a call, a source terminal notifies a network of the priority such

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that cells of the connection having the lowest priority are first discarded in conformity to the priority information specified by the sub-class, when a best effort control traffic class falls into congestion, to  
5 exclude cells of connections having higher priorities from cells to be discarded even if the cells belong to the same traffic class. When the degree of congestion becomes more grave even though cells have been discarded, the cell discard is gradually performed on  
10 cells of connections having higher priorities. However, as the best effort traffic class is recovered from the congestion, the discard processing is stopped in order from cells of connections having higher priorities. In this way, the Quality of Service can be  
15 guaranteed for connections having higher priorities even within the best effort control traffic class group.

The foregoing and other objects, advantages, manner of operation and novel features of the present  
20 invention will be understood from the following detailed description when read in connection with the accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram illustrating an  
25 embodiment of a packet switching device 100 according to the present invention;

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Figs. 2A, 2B, 2C are diagrams illustrating formats of cells handled by the packet switching device 100 and a control message outputted from a terminal;

Fig. 3 is a block diagram illustrating an embodiment of an output buffer 107 in Fig. 1; and

Fig. 4 is a block diagram illustrating an embodiment of an input line interface 102 unit in Fig. 1.

#### 10 DESCRIPTION OF THE PREFERRED EMBODIMENTS

An ATM switching device comprising FIFO output buffers for controlling cell transfer with the CBR traffic class treated as a traffic having a higher priority will be described as an embodiment of the present invention.

Fig. 1 illustrates an ATM switching device (switch) 100 according to the present invention which is connected to N input lines and N output lines.

While a network configuration including two terminal devices A and B 162, 164 connected to the switching device through input/output lines (subscriber lines) is illustrated for convenience of explanation, a portion of the input/output cables may be a trunk for connecting the switching device to another switching device. Also, in this example, the configuration is illustrated such that the terminal A 162, located on the left side of the switch 100, transfers cells to the

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terminal B 164 located on the right side of the switch 100. However, in an actual switching device, an ith input line forms a pair with an ith output line, and an output cell from the first output line in Fig. 1 is inputted to the terminal A, while a transferred cell from the terminal B is inputted to an Nth input line.

The switch 100 is composed of a plurality of input line interface units (LIFi) 102 (102-1 ~ 102-N) disposed corresponding to the respective input lines, a switch core unit 120, a plurality of output line interface units (LIFO) 108 (108-1 ~ 108-N) disposed corresponding to the respective output lines, and a call control unit (connection processing unit: CP) 140.

Each of the input line interface units 102 is composed of a header conversion circuit 132, a cell discard judgement unit 136, and a cell buffer 134. Also, the switch core unit 120 is composed of a crossbar switch circuit 105, a plurality of FIFOs 107 (107-1 ~ 107-N) provided corresponding to respective output lines, and a congestion status measurement circuit 106 connected to the respective FIFOs 107.

Fig. 2A illustrates a format of a cell inputted from each input line to the input line interface unit 102 of the switch 100.

A message transmitted from the terminal A to the terminal B is divided into a plurality of data blocks having a fixed length, and a cell header is

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added to each data block to form the cell 210. Each cell 210 includes a header portion and a data portion 212, and the header portion includes an input VCI 216, and information (PTY) 214 indicating the position of a data block included in the data portion within a packet (transmission message) treated by a higher rank protocol. Assume in the following description that a cell including a data block at the head of an upper rank packet is referred to as a "packet delimiter".

10           The header conversion circuit 132, when an input cell 210 is inputted thereto from an input line, reads header conversion information corresponding to the input VCI 216 of the inputted cell 210 from a header conversion table to convert the header into an internal cell 220 in a format illustrated in Fig. 2B.

15           Added to a header portion of the internal cell 220 are an output VCI 221 for replacing the input VCI 216 of the input cell 210, routing information (output port information ) 222, a traffic class 224, a sub-class 225, and a packet discard status information 228 indicating whether or not a packet associated with the VCI is discarded or not. Unless congestion occurs at an associated output port, the internal cell 220 is sent to the switch core unit 120 without being  
20 discarded, and driven into a particular output buffer FIFO 107 indicated by the routing information (output port information) through the crossbar switch circuit 105.

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Fig. 2C illustrates a control message  
(connection information) 230 for setting up a call to  
be sent from the source terminal 162 to the switch 100  
prior to a communication with the receiving terminal  
5 164.

The connection information 230 includes  
destination address information 232 for specifying a  
destination terminal, traffic class information 234,  
sub-class information 236 for indicating a priority  
10 relative to cell discard, and terminal protocol  
information 238 indicative of an upper rank protocol at  
the source terminal. The connection information 230 is  
divided into a plurality of fixed-length blocks in the  
source terminal, and a cell header is added to each  
15 block to be formed into a control cell, having a  
similar format to that illustrated in Fig. 2A, which is  
then driven into the switch 100.

The control cell is transferred from the  
switch core unit 120 to the call control unit  
20 (connection processing unit: CP) 140 through a signal  
processing means, omitted in Fig. 1. The signal  
processing means is provided to assemble the contents  
(data block) of a data portion 212 of each control cell  
into the original connection information (message form)  
25 illustrated in Fig. 2C, and may be configured as part  
of the call control unit 140.

The call control unit 140 sets the output VCI  
226 allocated to a call, output port information 222

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specified by the destination address, and a traffic class 234 and a traffic sub-class 236 extracted from the connection information 230 to a conversion table (not shown) of the header conversion circuit 132

5 connected to the source terminal in a call setup sequence executed in response to the connection information.

When a call (connection) setup is completed between the terminals, the source terminal 162 starts  
10 sending cells (user cells) 210 to the destination terminal 164.

Fig. 3 illustrates an exemplary configuration of the FIFO output buffer 107.

The FIFO buffer 107 is composed of two FIFOs  
15 301, 302, one for CBR and the other for UBR, and an FIFO control circuit 310. The FIFO control circuit 310 preferentially outputs cells accumulated in the CBR FIFO 301 prior to cells accumulated in the UBR FIFO 302.

20 In a normal state in which no congestion occurs, user cells outputted from each FIFO output buffer 107i are inputted to an associated line output control unit LIFO 108i which discards unnecessary internal header information 222 - 228 and sends the  
25 user cell in the output cell format including information elements 212 - 221 onto an output line.

The cell accumulation status (congestion status) in each of two FIFOs 301, 302 for CBR and UBR

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traffic classes of each FIFO output buffer is collected in the congestion status measurement circuit 106 through a signal line 156. The congestion status measurement circuit 106 edits the cell accumulation status into congestion status information corresponding to an output port, and notifies each of the input line interface units 102-1 ~ 102-N of the congestion status information through a signal line 152. For example, the congestion status measurement circuit 106 classifies the cell accumulation status at each output port into "no congestion", "light congestion", and "heavy congestion" and edits the cell accumulation status as the congestion status information.

Fig. 4 illustrates in detail the configuration of the input line interface unit 102, particularly, the cell discard judgement unit 136. The cell discard judgement unit 136 comprises a packet discard judgement circuit 410, a packet delimiter detection circuit 420, and a congestion level judgement circuit 430.

The congestion level judgement circuit 430 fetches an output port 222, a traffic class 224, and a traffic sub-class 225 from among information elements of each input cell converted into an internal cell format by the header conversion circuit 132, judges whether or not discard of the input cell is necessary based on the fetched information and congestion status

information 152 provided from the congestion status measurement circuit 106, and provides the packet discard judgement circuit 410 with a discard control signal 153 in accordance with the judgement result.

- 5 The congestion level judgement circuit 430 utilizes discard sub-class information 432, which is counted up or down in accordance with the congestion status information 152, to determine a mode for the discard control signal 153.

- 10 For example, when the traffic class 224 of an input cell is a best effort traffic class, the congestion level judgement circuit 430 pays attention to congestion status information 152' of a particular output port corresponding to the output port
- 15 information 222 of the input cell within the congestion status information provided from the congestion status measurement circuit 106, and counts down the discard sub-class information 432 corresponding to the particular output port when the congestion control
- 20 information 152' indicates "no congestion", maintains a current value of the discard sub-class information 432 when the congestion control information 152' indicates "light congestion", and counts up the discard sub-class information 432 when the congestion control information
- 25 152' indicates "heavy congestion". Next, the congestion level judgement circuit 430 compares the value of a cell discard priority indicated by the

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traffic sub-class 225 of the input cell with the discard sub-class information 432, and sets the discard control signal 153 in a "pass mode" when the priority is larger than the value of the discard sub-class information 432, in a "packet unit discard mode" when equal, and in a "all cell discard mode" when smaller.

The packet delimiter detection circuit 420 judges PTY 214 of an input cell in the internal cell format outputted from the header conversion circuit 132, and turns on a signal 440 indicative of a delimiter of a packet when the input cell includes a head block of the packet in a data portion 212. The signal 440 is provided to the packet discard judgement circuit 410.

The packet discard judgement circuit 410 judges whether or not the input cell should be discarded based on the discard control signal 153, the packet delimiter signal 440, and the packet discard status information 228 extracted from the input cell in the internal cell format outputted from the header conversion circuit 132, and generates a cell discard instruction signal 154 as described in detail in the following (a) - (d). The cell buffer (packet discard means) 134 selectively passes or discards the input cell in accordance with the cell discard instruction signal 154.

(a) When the packet discard status information 228 included in the header portion of an

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input cell indicates "packet under discard", the cell discard instruction signal 154 is turned on irrespective of the status of the cell discard control signal 153. With the cell discard instruction signal 154 turned on, subsequent input cells including data blocks of the same packet are sequentially discarded.

(b) When the cell discard control signal is in the "pass mode", the cell discard instruction signal 154 is turned off except for the case where an input cell falls under the condition (a). In this case, input cells are written into the buffer 134 and then supplied to the switch core unit 120.

(c) When the cell discard control signal 153 is in the "packet discard mode", the cell discard instruction signal 154 is turned on at the time the packet delimiter signal 440 is turned on. For a packet, the data block of which has partially passed, subsequent cells are not subjected to the discard processing. At the time the cell discard instruction signal 154 is turned on, the header conversion circuit 132 is provided with a header conversion table rewrite instruction 450 to set the packet discard status information 228 in the cell header in "packet under discard", such that all of subsequent cells including data blocks of the same packet are discarded under the condition (a).

(d) When the cell discard control signal 153 is in the "all cell discard mode", the cell discard

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header, passes subsequent cells of a packet, the cells of which have partially passed, and starts the discard processing from the head cell of a newly arriving packet, the packet discard mode may be modified to

5 immediately start discarding cells at the time congestion has occurred, and pass cells of a new packet and discard subsequent cells of a packet, the cells of which have been partially discarded, when the congestion is solved. Further, these packet discard

10 modes may be switched in accordance with a congestion status.

As is apparent from the foregoing embodiment, according to the present invention, a cell discard priority is declared as sub-class information in a

15 traffic class, upon setting up a call, and stored in a node such as an ATM switch and so on, for a best effort control traffic class group which has difficulties in making bandwidth reservation from a terminal unit upon setting up a call. With the cell discard priority,

20 when congestion occurs in a node, cells of connections with lower priorities are preferentially discarded, thereby making it possible to protect traffics of connections with higher priorities.

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